



Parallel Programming and Heterogeneous Computing

FPGA Accelerators - Metal FS Hands-On

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Getting started with Metal FS

The Metal FS project lives on GitHub:

<https://github.com/osmhpri/metalfs>

Watch the intro video first (link on <https://osm.hpi.de/parProg>)!

We are going to follow a modified version of this tutorial:

<https://metalfs.github.io/tutorial.html>

To follow along, you will need a platform with Docker and docker-compose.

**ParProg 2020 C3
FPGA Accelerators**

Lukas Wenzel

Chart 2

Getting started with Metal FS

Build a Metal FS operator to filter unsigned 32-bit values.

00000004	00000008
0000000c	0000000f
00000001	00000007
0000000d	00000005
00000000	00000003
0000000a	00000002
00000006	0000000e
00000009	0000000b

input.bin

```
$ cat input.bin  
| filter -l 4 -u 12  
> output.bin
```

00000004	00000008
0000000c	00000007
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00000006	00000009
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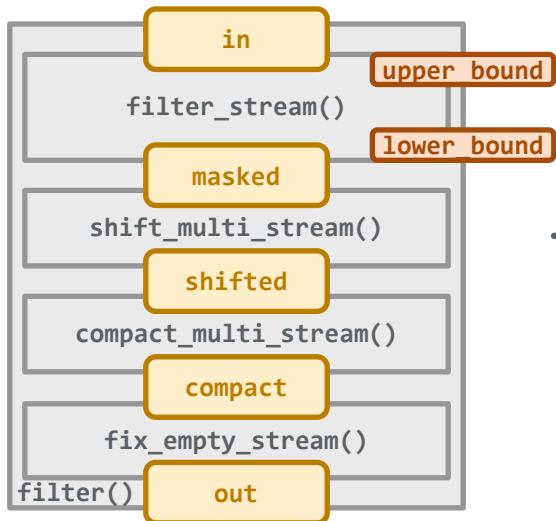
output.bin

**ParProg 2020 C3
FPGA Accelerators**

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Chart 3

Getting started with Metal FS



```

void filter(mtl_stream &in, mtl_stream &out,
            Value lower_bound, Value upper_bound) {
    #pragma HLS INTERFACE axis port=in name=axis_input
    #pragma HLS INTERFACE axis port=out name=axis_output
    #pragma HLS INTERFACE s_axilite port=lower_bound bundle=control offset=0x100
    #pragma HLS INTERFACE s_axilite port=upper_bound bundle=control offset=0x110
    #pragma HLS INTERFACE s_axilite port=return bundle=control

    MaskedStream masked;
    ShiftedStream shifted;
    mtl_stream compact;

    #pragma HLS DATAFLOW

    filter_stream(in, masked, lower_bound, upper_bound);

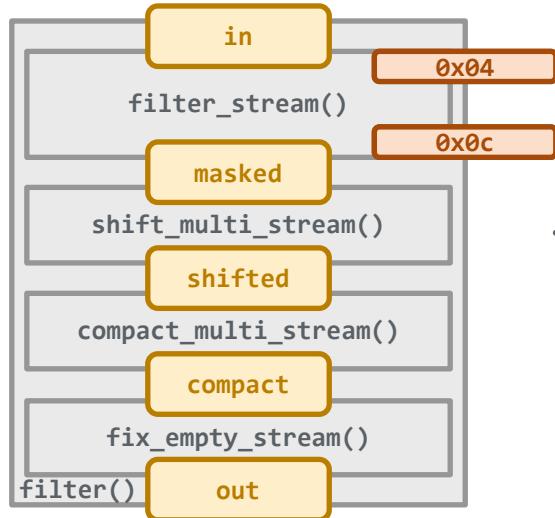
    #if VALUE_COUNT > 1
        shift_multi_stream(masked, shifted);
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    #endif

    fix_empty_stream(compact, out);
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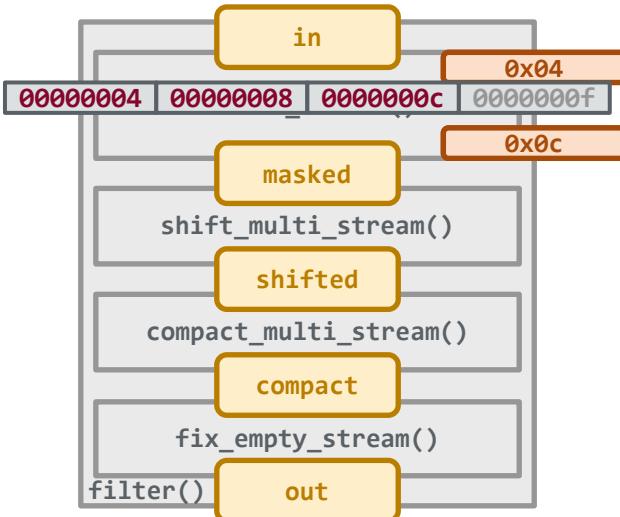
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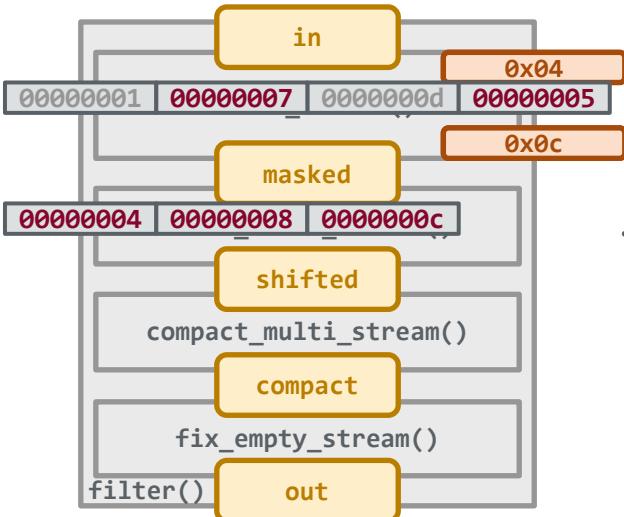
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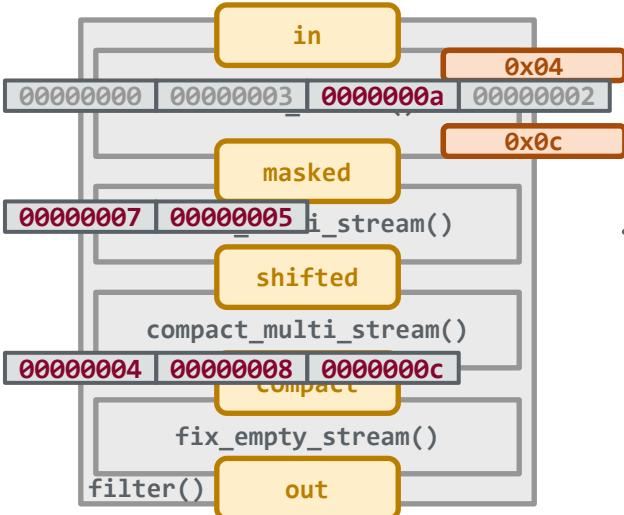
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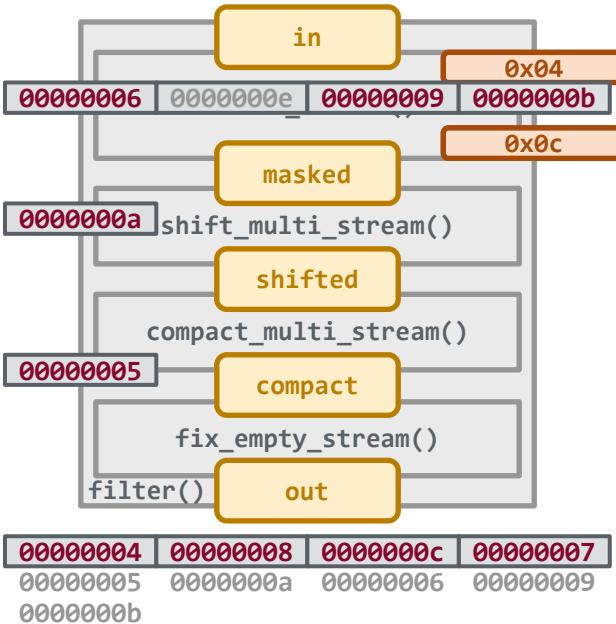
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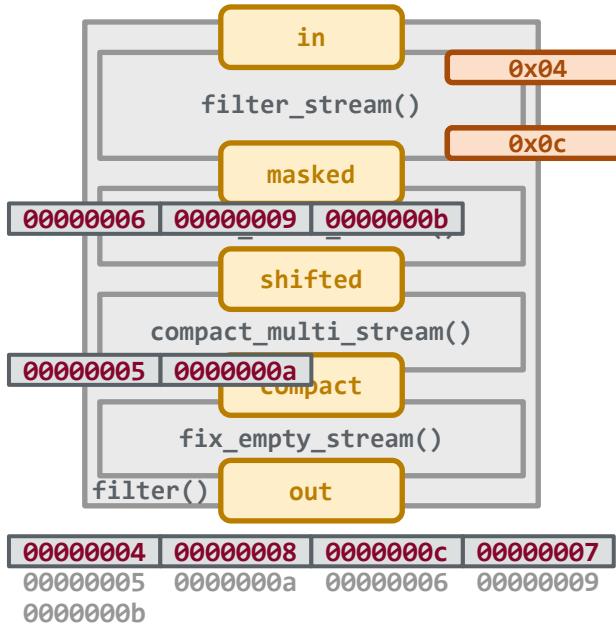
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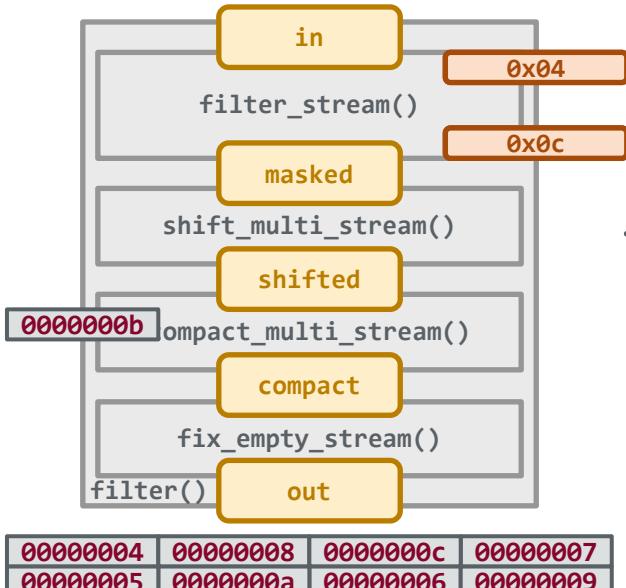
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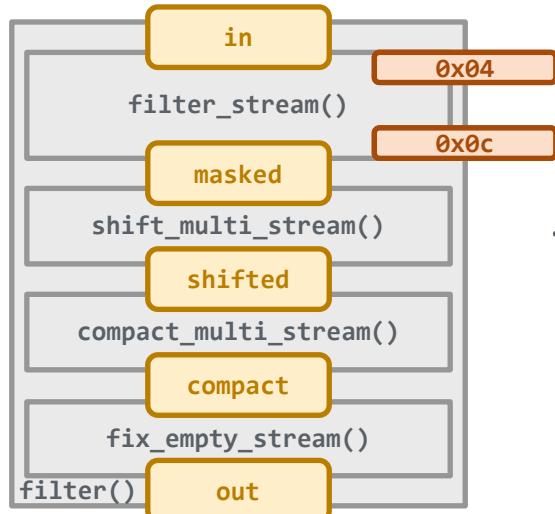
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```

And now for a break and
some cold brewed Bancha.



*or beverage of your choice